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ABSTRACT OF THE DISCLOSURE

A bypass circuit around a filter and RF amplifier of a front end device is activated when a received signal strength (RSS) passes a threshold in which the filter and RF amplifier are not necessary to filter/boost the received signal for processing in subsequent electronics (mixers, SAW filters, AGC amps, etc). A control device compares the RSS to the threshold and activates the bypass circuit. The bypass circuit is a switch coupled between an input of the filter and an output of the RF amplifier. The RF amplifier is shutdown when the bypass circuit is activated. The present invention may be used in combination with an LNA bypass to further decrease power consumption.